COURSE DESCRIPTION CARD - SYLLABUS

Course name
Microprocessors [S1EiT1>MIKRO2]

Course

Field of study
Electronics and Telecommunications

Year/Semester
3/5

Area of study (specialization)
–

Profile of study
general academic

Level of study
first-cycle

Course offered in
Polish

Form of study
full-time

Requirements
compulsory

Number of hours

Lecture
0

Laboratory classes
30

Other (e.g. online)
0

Tutorials
0

Projects/seminars
0

Number of credit points

2,00

Coordinators

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Lecturers

Prerequisites

Essentials on digital circuits and software development (writing the sorce code)

Course objective

The course objective is to acquaint the students with hardware, applications, and programming of selected microprocessors and microcontrollers

Course-related learning outcomes

none

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

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A written exam takes place after semester 4. On the exam the students are asked to briefly describe the results of attached assembler code examples. There are also some questions related to the microprocessor architecture and/or some peripherals. The students can obtain 0-1 or 0-2 points for each

question, depending on its difficulty; to pass, they must obtain at least half of available points.
To evaluate the students" progress on the tutorials, two written tests, covering the tutorial topics, are organized.
During the lab classes, offered in semester 5, the students" progress is evaluated judging by their diligence and the quality of developed code. A few ad-hoc tests are also planned to verify if the students have gathered necessary knowledge.

Programme content

Programmers' model and architecture of selected microprocessors, code development.

Course topics

Lecture: introduction to microprocessors and microcontrollers; Intel 8051/52 microcontrollers: the architecture, a set of control instructions, assembler programming, programming tools, application examples; review of 80x86 Intel microprocessors; ARM Cortex-M3/M4: architecture, register stack, program development tools, application examples.
Tutorials: developing microcontroller programs by writing assembler code.
Lab classes: 8051/52 and ARM code development and debugging, Code Composer Studio

Teaching methods

Conventional lecture: architecture of microcontrollers is presented basing on available datasheets, some assembler code examples are discussed.
Lab classes: individual students" work with PCs and evaluation boards featured with either 8051 or Cortex-M4 microprocessors. The students develope their own code, upload it onto the board and evaluate its performance. The teacher suggests improvements and is responsible for final assessment of the students" code. At the beginning, the students write the assembler code. At the second stage, they develop the code written in C language and debug it on hardware by means of Code Composer Studio application.

Bibliography

Basic:
- "MCS-8051 Microcontroller"s Family User Manual", "ARM Cortex-M for Beginners", "Application Note 237 - Migrating from 8051 to Cortex Microcontrollers", "ARM and Thumb-2 Instruction Set Quick Reference Card", all available on the Internet
- datasheets related to Intel 16- and 32-bit microprocessors (distributed by the teacher), and selected 64-bit microprocessors, Atmel SAM3S1A datasheet (all available on the web)
Additional:
- any handbook or other literature/web sources related to microprocessors and microcontrollers presented in class.

Breakdown of average student's workload

	Hours	ECTS
Total workload	0	0,00
Classes requiring direct contact with the teacher	0	0,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	0	0,00